

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3050	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:19
S2	3612	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:00
S3	1095	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:02
S4	46782	S1 or (refresh\$3 and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:02
S5	1141	(S2 S3) and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:10
S6	893430	(module or pcb or (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:11
S7	240623	S6 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:11
S8	314	S5 and S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:11

S9	147	(refresh\$3 near3 control\$4) and S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S10	9793	(refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S11	3050	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S12	3612	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S13	1095	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S14	46782	S11 or (refresh\$3 and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S15	1141	(S12 S13) and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S16	893430	(module or pcb or (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20

S17	240623	S16 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S18	314	S15 and S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:20
S19	147	(refresh\$3 near3 control\$4) and S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:24
S20	147	S10 and S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:21
S21	147	S10 and S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:21
S22	9646	S10 not S21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:21
S23	147	S10 not S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:23
S24	3800	(365/233 365/236).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:24

S25	4192	(365/233 365/236 365/239).ccs.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:24
S26	828	(refresh\$3 near3 control\$4) with (chip IC (integrated adj3 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:25
S27	19	S23 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:26
S28	310	(separat\$3 dedicat\$3) with (refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:27
S29	218	(separat\$3 dedicat\$3) near5 (refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 13:27
S30	14	S29 and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 16:40
S32	20	S23 and S25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 14:01
S33	451	S13 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 14:34

S34	5	S11 and S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 14:35
S38	4823	(361/728 361/736 361/748 361/760 361/761).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:13
S39	140790	("361"/\$).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:13
S40	1	S10 and S38	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:14
S41	24372	S39 and (module pcb (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:14
S42	15	S10 and S41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:14
S43	20	(refresh\$3 near3 (control\$4 logic)) near3 generat\$3 with independent\$2 with (signal command)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 15:57
S44	0	(refresh\$3 near3 control\$4) same (receiv\$3 near3 input) same (generat\$3 with command with independent\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 16:42

S45	0	(refresh\$3 near3 control\$4) same (receiv\$3 near3 input) same (generat\$3 with command same independent\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 16:42
S46	3050	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11
S47	3612	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11
S48	1095	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11
S49	46782	S46 or (refresh\$3 and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11
S50	1141	(S47 S48) and S49	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11
S51	310	(separat\$3 dedicat\$3) with (refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11
S52	21	S51 and S50	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 17:11

S53	8	(refresh\$3 near3 control\$4) with (separat\$3 near3 chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 18:57
S54	16	(US-20020176308-\$ or US-20020118591-\$ or US-20020075746-\$ or US-20040027876-\$ or US-20030145163-\$).did. or (US-6104657-\$ or US-6091660-\$ or US-5818777-\$ or US-6873563-\$ or US-6385113-\$ or US-5629898-\$ or US-5550781-\$ or US-5959923-\$ or US-5731945-\$ or US-5345574-\$ or US-4965722-\$).did.	US-PGPUB; USPAT	OR	ON	2005/11/11 19:23
S55	0	S54 and (DIMM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:23
S56	4823	(361/728 361/736 361/748 361/760 361/761).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:23
S57	56	S56 and (DIMM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:28
S58	310	(separat\$3 dedicat\$3) with (refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:24
S59	0	S57 and S58	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S60	3050	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
			US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB			

S61	3612	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S62	1095	711/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S63	46782	S60 or (refresh\$3 and memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S64	1141	(S61 S62) and S63	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S65	893430	(module or pcb or (printed near3 circuit near3 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S66	240623	S65 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S67	314	S64 and S66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S68	147	(refresh\$3 near3 control\$4) and S67	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25

S69	0	S57 and S68	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:25
S70	206	S67 and S61	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:26
S71	0	S57 and S61	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:26
S72	0	S57 and (refresh\$3 near3 control\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:29
S73	20	S57 and (controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:30
S74	4	S57 and (controller) and refresh\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 19:30